

THE HIGH-END VIRTUALIZATION COMPANY SERVER AGGREGATION – CREATING THE POWER OF ONE

Virtual SMP with vSMP Foundation

Nir Paikowsky Director of Application Engineering



- 1. INTRODUCTION TO SCALEMP
- 2. PRODUCT OVERVIEW
- 3. HOW DOES IT LOOK
- 4. TYPICAL USE CASES
- 5. HOW DOES IT WORK
- 6. PERFORMANCE



INTRODUCTION



Aggregate. Scale. Simplify. Save.

3/19/2010 3 Confidential and Proprietary

ScaleMP at a Glance

- Founded in 2003
- Product shipping since 2006
- Sold through Tier-1 and Tier-2 OEMs

Virtualization for high-end computing, delivering higher performance and lower Total Cost of Ownership (TCO)

Aggregation software creates a virtual shared-memory multi-processor (SMP) from multiple off-the-shelf x86 servers

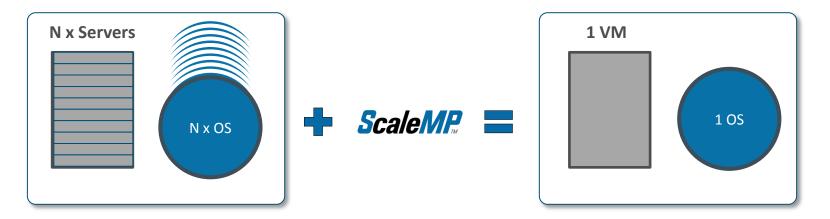


150+ Deployments Worldwide





What We Do



Virtualization software for aggregating multiple off-the-shelf systems into a single virtual machine, providing improved usability and higher performance

Targeting compute-, memory- and I/O-intensive workloads





PRODUCT OVERVIEW

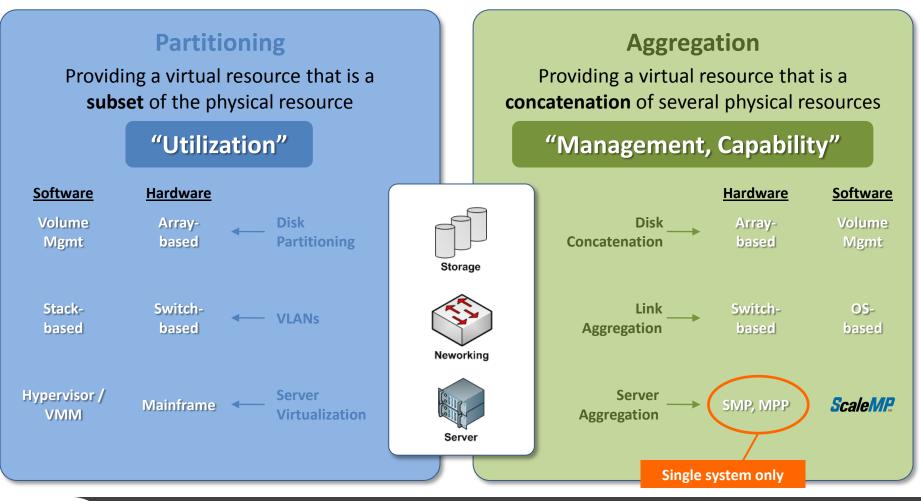


Aggregate. Scale. Simplify. Save.

3/19/2010 6 Confidential and Proprietary

Virtualization Across Different Domains...

...and comparing partitioning and aggregation approaches

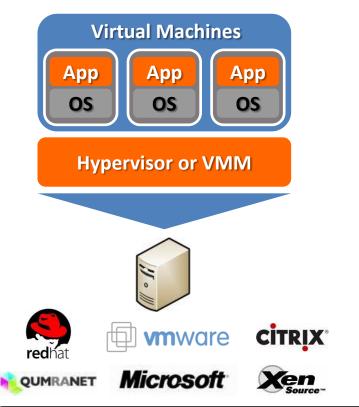




Approaches to Server Virtualization

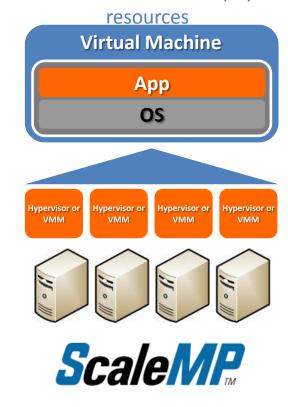
Partitioning

Providing a virtual resource that is a **subset** of the physical resource



Aggregation

Providing a virtual resource that is a **concatenation** of several physical







HOW DOES IT LOOK



Aggregate. Scale. Simplify. Save.

3/19/2010 9 Confidential and Proprietary

TOP and FREE

E ~														
Tasks Cpu(s Mem: Swap:	: 1383 t >: 0.0% 6608133	otal, us, Ø 32k to Øk tot	1 .0%s tal, al,	runnir 9, 0. 5347	ng, 13 .0%ni, 7508k 0k u	82 slee 100.0% used, 6 used,	ping, id, (555469	. 0 0.0%wa 5824k 0k fr	free, 1 ee, 1269	0 zombie 0.0%si, 6096k buf: 96k cache	fers d			
	USER	PR	NI	VIRT	RES	SHR S			TIME+	P COMMA	ND			
23456789011121345678901112134451678900111213445112900000000000000000000000000000000000	root root root root root root root root	158 1814 34 174 34 34 34 34 34 34 34 34 34 34 34 34 34		13676400000000000000000000000000000000000	2112 26 2000 2000 2000 2000 2000 2000 20	ਸ਼ਲ਼ 988 956 856	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		0:00.20 0:38.39 0:00.02 0:00.02 0:00.02 0:00.02 0:00.01 0:00.00	1 ksoft 1 watch 2 migra 2 ksoft 2 watch 3 migra 3 ksoft 4 migra 4 ksoft 4 watch	irqd/0 dog/0 tion/1 irqd/1 tion/2 tion/2 dog/2 tion/3 irqd/3 dog/3 tion/4 irqd/4 dog/4 tion/5 irqd/5 dog/5 tion/6			
22	root	RT	0		E /mind									
	root	RT	0		/cyga	rive/d/SM	P/tmp							
24	root	39	19	Me -, Se	em: /+ buł wap:	lash-1-2 ffers/ca lash-1-2	total 630 ache: 0		-g used 5 4 Ø	free 624 625 Ø	shared Ø	buffers Ø	cached Ø	



/proc/cpuinfo

	E ~	8		_
	processor	:	0	
	vendor_id	=	GenuineIntel	
	cpu family	=	6	
	model	=	26	
	model name	=	Intel(R) Xeon(R) CPU E5530 @ 3	2.40GHz
	stepping	=	5	
	cpu MHz			
	cache size		8192 KB	
	physical id	-		
	siblings	=		
	core id	Ξ	0	
	cpu cores			
	cpu cores		8	
	apicid	=		
	քքա		J = -	
	fpu_exception		yes	
	cpuid level			
l	μp		yes	
l	flags	=	fpu vme de pse tsc msr pae mce cx8 apic :	sep_mtrr pge
	ant_tsc pni ds_c		1 vmx est tm2 ssse3 cx16 xtpr dca popcnt	lahf_lm
	podowibs		4820.91	
	clflush_size		64	
	cache_alignment			
	address sizes		40 bits physical, 48 bits virtual	
	power management			
	processor		-	
	vendor_id		GenuineIntel	
	cpu family		6	
	model		26	
	model name			2.40GHz
	stepping			
	cpu MHz		2400.083	
	cache size		8192 KB	
	physical id		0	
	siblings			
	core id			
	cpu cores		8	
	cpu cores	-		
	apicid	Ξ		
	քքա			
	fpu_exception		yes	
	cpuid level		11	
			lies	

processor processor		59 60 61 62 63 64 65 65 65
processor processor		61 62 63 64 65 66
processor processor		64 65 66
processor processor		64 65 66
processor processor		64 65 66
processor processor		65 66
processor processor		66
processor processor	:	
processor processor		67
processor processor		68 69
processor processor		70
processor processor	-	71
processor processor		72
processor processor		73
processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor		74
processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor	-	75 76
processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor	-	77 77
processor processor processor processor processor processor processor processor processor processor processor processor processor processor processor	-	78
processor processor processor processor processor processor processor processor processor processor processor processor processor		79
processor processor processor processor processor processor processor processor processor processor processor		80
processor processor processor processor processor processor processor processor processor processor processor		81
processor processor processor processor processor processor processor processor processor	-	82 83
processor processor processor processor processor processor processor processor	-	84
processor processor processor processor processor processor processor	-	85
processor processor processor processor processor processor		86
processor processor processor processor processor		87
processor processor processor		88
processor processor	-	89
processor	-	90 91 92 93 94 95 96 97 98
	-	92
		93
processor		94
processor	÷	95
processor processor	-	30 07
processor	-	98
processor		<u>99</u>
processor		99 100
processor		101
processor	-	102 103
processor processor	-	103
processor processor	-	105
processor		106
processor		107
processor	÷	108
processor	-	109 110
processor processor	-	111
processor	-	112
processor		112 113 114
processor		114
processor	5	115 116
processor processor	-	116 117
processor processor	-	117
processor	-	119
processor		120
processor		121
processor	5	122 123
processor processor	-	123 124
processor	-	125
processor		
processor [root@dash=1=20	~	126



VSMPVERSION

e ~

[root@dash-1-20 ~]# vsmpversion vSMP Version: 2.1.85.29 vSMP Foundation: 2.1.85.29 (Mar 09 2010 16:23:56) System configuration: Boards: 16 (out of 16) 32 x Intel(R) Xeon(R) CPU E5530 @ 2.40GHz (cores: 4) Processors: 16 x 49144MB Memory: Total memory: 786304MB vSMP Foundation: 12352MB Reserved for cache: 118592MB System memory: 655360MB [HDDØ] ATA ST9250421AS Boot device: Serial number: 1000101 System key: DRN9R-BYEY7-I2EP1-3LJJD-9KU1M-P52 Supported until: Jul 1 2012 vsmpctl Version: 42.1.0 (Dec 14 2009 16:27:33) HWI Version: 8(4) [root@dash-1-20 ~]#



/sbin/lspci -vt

_

E ~	
-+-[0000:f0]-+-00.0 Intel Corporation	X58 IZO Hub to FSI Port
+-03.0-[0000:f5]+-00.0	Intel Corporation 82576 Gigabit Network Connection
	Intel Corporation 82576 Gigabit Network Connection Intel Corporation 82576 Gigabit Network Connection
+-1f.0 Intel Corporation	82801JIR (ICH10R) LPC Interface Controller
\-1f.2 Intel Corporation	82801JI (ICH10 Family) SATA AHCI Controller
+-[0000:e0]-+-00.0 Intel Corporation	X58 I/O Hub to ESI Port
+-03.0-[0000:e5]+-00.0	Intel Corporation 82576 Gigabit Network Connection Intel Corporation 82576 Gigabit Network Connection
↓ \-00.1	Intel Corporation 82576 Gigabit Network Connection
+-1f.0 Intel Corporation	82801JIR (ICH10R) LPC Interface Controller
\-1f.2 Intel Corporation	82801JI (ICH10 Family) SATA AHCI Controller
+-[0000:d0]-+-00.0 Intel Corporation	X58 I/O Hub to ESI Port
+-03.0-[0000:d5]+-00.0	Intel Corporation 82576 Gigabit Network Connection Intel Corporation 82576 Gigabit Network Connection 82801JIR (ICH10R> LPC Interface Controller 82801JI (ICH10 Family) SATA AHCI Controller X58 I/O Hub to ESI Port Intel Companying 20576 Gigabit Network Connection
	Intel Corporation 82576 Gigabit Network Connection
+-1f.0 Intel Corporation -1f.2 Intel Corporation	82881JIN (IGHIMA) LFG INCEPTACE CONCRUIER
+-[0000:c0]-+-00.0 Intel Corporation	VEO LA UNE to ESI Dant
	Intel Corporation 82576 Gigabit Network Connection
	Intel Corporation 82576 Gigabit Network Connection
+-1f.0 Intel Corporation	82801JIR (ICH10R) LPC Interface Controller
\-1f.2 Intel Corporation	82801JI (ICH10 Family) SATA AHCI Controller
+-[0000:b0]-+-00.0 Intel Corporation	X58 I/O Hub to ESI Port
+-03.0-[0000:b5]+-00.0	Intel Corporation 82576 Gigabit Network Connection
	Intel Corporation 82576 Gigabit Network Connection Intel Corporation 82576 Gigabit Network Connection
+-07.0-[0000:b3]00.0	LSI Logic / Symbios Logic SAS1068E PCI-Express Fusion-MPT SAS
+-1f.0 Intel Corporation	LSI Logic / Symbios Logic ŠAS1068E PCI-Express Fusion-MPT SAS 82801JIR (ICH10R) LPC Interface Controller
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	82801.II (ICH10 Familu) SOTO OHCI Contwollow
+-[0000:a0]-+-00.0 Intel Corporation	X58 I/O Hub to ESI Port
+-03.0-[0000:a5]+-00.0	82801JI (Ichið Family) ann and Controller Intel Corporation 82576 Gigabit Network Connection 82801JIR (ICHI0R) LPC Interface Controller 82801JI (ICHI0 Family) SATA AHCI Controller
\−00.1	Intel Corporation 82576 Gigabit Network Connection
+-1f.0 Intel Corporation -1f.2 Intel Corporation	82801JIR (ICH10R) LPC Interface Controller
i N-1f.Z Intel Corporation	82801JI (ICHI0 FAMILY) SHIH HHGI CONTPOLLER
+-[0000:90]-+-00.0 Intel Corporation	X58 I/O Hub to ESI Port Intel Corporation 82576 Gigabit Network Connection
	Intel Corporation 82576 Gigabit Network Connection
+-1f.0 Intel Corporation	82801JIR (ICH10R) LPC Interface Controller
1 11.8 Intel Corporation	82801JI (ICH10 Family) SATA AHCI Controller
	X58 I/O Hub to ESI Port
+-03.0-[0000:85]+-00.0	Intel Corporation 82576 Gigabit Network Connection
\−00.1	Intel Corporation 82576 Gigabit Network Connection Intel Corporation 82576 Gigabit Network Connection
+-1f.0 Intel Corporation	82801JIR (ICH10R) LPC Interface Controller
\-1f.2 Intel Corporation	82801JI (ICH10 Family) SATA AHCI Controller
+-[0000:70]-+-00.0 Intel Corporation	X58 I/O Hub to ESI Port
+-03.0-[0000:75]+-00.0	Intel Corporation 82576 Gigabit Network Connection
-00.1	Intel Corporation 82576 Gigabit Network Connection
+-1f.0 Intel Corporation	Ass 1/0 mub to Est port Intel Corporation 82576 Gigabit Network Connection Entel Corporation 82576 Gigabit Network Connection 82804JJR (ICH100R) LPC Interface Controller 82804JJI (ICH10 Family) SATA AHCI Controller
\-1f.2 Intel Corporation	82801J1 (ICH10 Family) SATA AHCI Controller
LOODO COlOO.O INCEL CORPORATION	ASO I/O HUD LO ESI FUFL
+-03.0-10000:651+-00.0	Intel Corporation 82576 Gigabit Network Connection
	Intel Corporation 82576 Gigabit Network Connection
+-1f.0 Intel Corporation	82801JIR (ICH10R) LPC Interface Controller
	82801JI (ICH10 Family) SATA AHCI Controller
+-10000:50] +-00.0 Intel Corporation +-03.0-[0000:55]+-00.0	X58 I/O Hub to ESI Port
	Intel Corporation 82576 Gigabit Network Connection Intel Corporation 82576 Gigabit Network Connection
+-1f.0 Intel Corporation	82801JIR (ICH10R) LPC Interface Controller
! _1f 2 Intel Componstion	82801JI (ICH10 Esmilu) SOTO OHCI Contwolley
+-[0000:40]-+-00.0 Intel Corporation	458 I/O Hub to FSI Port
+-03.0-[0000:45]+-00.0	Intel Corporation 82576 Gigabit Network Connection
	8280101 (Inite Family) shin and controller Intel Corporation 82576 Gigabit Network Connection 82801JIR (ICHIOR) LPC Interface Controller 82801JI (ICHIO Family) SATA AHCI Controller
+-1f.0 Intel Corporation	82801JIR (ICH10R) LPC Interface Controller
+-1f.0 Intel Corporation -1f.2 Intel Corporation	82801JI (ICH10 Family) SATA AHCI Controller
+-[0000:30]-+-00.0 Intel Corporation	X58 I/O Hub to ESI Port Intel Corporation 82576 Gigabit Network Connection
+-03.0-[0000:35]+-00.0	Intel Corporation 82576 Gigabit Network Connection
	Intel Corporation 82576 Gigabit Network Connection
+-1f.0 Intel Corporation	82801JIR (ICH10R) LPC Interface Controller
\-1f.2 Intel Corporation	82801JI (ICH10 Family) SATA AHCI Controller
+-[0000:20]-+-00.0 Intel Corporation	X58 I/O Hub to ESI Port Intel Corporation 82576 Gigabit Network Connection
+-03.0-10000:251+-00.0	Intel Corporation 82576 Gigabit Network Connection
	Intel Corporation 82576 Gigabit Network Connection



/proc/partitions

E ~	Section.		
			at /proc/partitions
majo	r minor	#blocks	name
8	Ø	244198584	sda
ਗ਼ਗ਼ਗ਼ਗ਼ਗ਼ ਗ਼ਗ਼ਗ਼ਗ਼ਗ਼ ਗ਼ਗ਼ਗ਼ਗ਼ਗ਼ਫ਼	ī	16386268	sda1
8	2	16386300	sda2
8	3	2048287	sda3
8	2 3 4 5 16	209375113	sda4 sda5
8	16	244198584	sdb
8	17	16386268	sdb1
8	18	16386300	sdb2
8	19 20	2048287	sdb3 sdb4
8	20	1 209375113	sab4 sdb5
8	32	62522712	sdc
8	48	62522712 244198584 62522712	sdd
8	64	62522712	sde
8	80 96	244198584 62522712	sdf
8	96 112	244198584	sdg sdh
8	128	62522712	sdi
8	144	244198584 62522712	sdj
8	160	62522712	sdk
8	176	244198584 62522712	sdl
8	192 208	62522712 244198584	sdn sdn
8 8	224	244198584 62522712	sdo
8	240	244198584	sdp
65	Ø	62522712	sdq
65	16	244198584	sdr
65	32 48	62522712 244198584	sds sdt
65	64	62522712	sdu
65	80	244198584 62522712	sdv
65	.96	62522712	sdw
65	$112 \\ 128$	244198584 62522712	sdx sdy
65	144	244198584	sdy sdz
65	160	244198584 62522712	sdaa
65	176	244198584	sdab
655 655 655 655 655 655 655 66	192 208	62522712 244198584	sdac
65	208	62522712	sdad sdae
65	240	244198584	sdaf
66	Ø	146484375	sdag
66	16	146484375	sdah
66	32 48	146484375 146484375	sdai
66	48 64	146484375	sdaj sdak
66	80	146484375	sdal
66	96	146484375	sdam
66	112	146484375	sdan
9	0 2	16386176 209375040	mdØ md2
- Š	1	16386176	md2 md1
		1-20 ~1#	





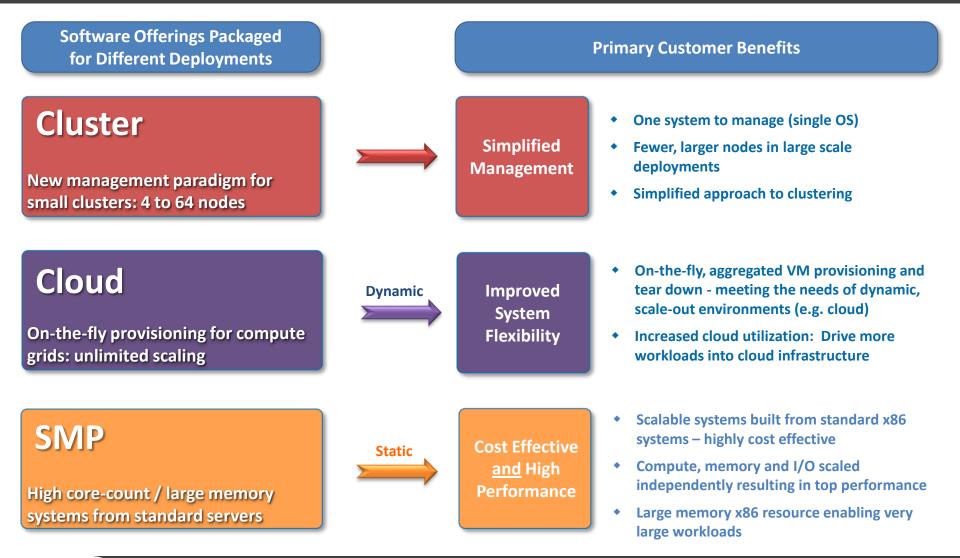
TYPICAL USE CASES



Aggregate. Scale. Simplify. Save.

3/19/2010 15 Confidential and Proprietary

Offerings and Customer Benefits





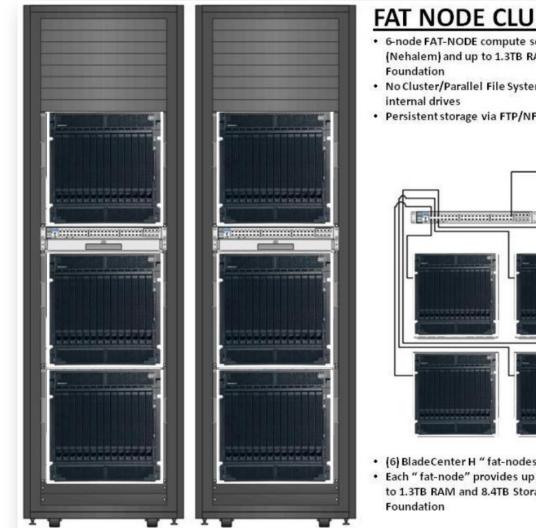
Solving Customer's Problems: Complexity

- Customer Pain:
 - Lack of in-house system administration & expertise:
 - Multi-system management
 - High-speed networks
 - Distributed file-systems
- vSMP Foundation Value:
 - Significantly reduce number of managed systems
 - Reduce the number of tools to operate the environment
 - Enable large/shared memory for performance acceleration and easier programming
- Result:
 - Lower operational costs associated with system management
 - InfiniBand performance without management overhead



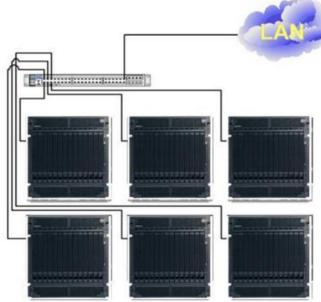


Cluster



FAT NODE CLUSTER

- 6-node FAT-NODE compute server, each with 112 cores (Nehalem) and up to 1.3TB RAM using blades and vSMP
- No Cluster/Parallel File System: Scratch storage using blades'
- Persistent storage via FTP/NFS over 1GbE or 10GbE



• (6) BladeCenter H " fat-nodes" connected by 10GbE network

· Each "fat-node" provides up to 112 cores (Nehalem) and up to 1.3TB RAM and 8.4TB Storage using blades and vSMP



Customer Use Cases

FINANCIAL SERVICES

- Customer: Hedge Fund
- Current platform: Multiple 4-Socket Servers
- Problems:
 - A single 4-socket server did not provide enough performance required for customer business targets
 - Co-location at exchanges for a solution comprised of multiple systems is complicate
 - Multiple 4-socket servers required complex decomposition and introduced challenges in transferring data between processes in a short and deterministic time (low latency and small jitters)
 - Ethernet based solution could not provide this / IB solution is too complex to manage and program for
- Applications: KX, WOMBAT, Home-grown code
- Solution:
 - 16 Intel dual-processor Xeon systems to provide 0.5TB RAM, 32 sockets (128 cores) single virtual system running Linux with vSMP Foundation
 - Alternative considered: IBM P5xx (POWER6). Too expensive and incompatible with x86 application base.
- Benefits:
 - Simpler solution: Deploy and management of a single system
 - **Simpler programming model:** No need for InfiniBand programming
 - **Better utilization:** Single system reduces resources fragmentation
 - Performance: Reduced latency and latency variance

SIMPLIFYING INTER-PROCESS COMMUNICATION





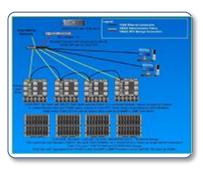
Customer Use Cases

ENGINEERING FACULTY

- Customer: Engineering Faculty
- Current platform: None. Just getting into HPC.
- Problems:
 - Compute requirements were growing, as number of users/students was growing
 - No in-house skills to run x86 InfiniBand cluster
 - Limited operational budget to hire additional sys-admin resources
- Applications: Commercial code, mostly Fluent and MATLAB
- Solution:
 - 4 full blade chassis, each aggregated as a single system with 128 cores and 384 GB RAM and 5 TB of internal storage
 - Total: 64 physical nodes, 512 cores, 20TB storage running as 4 fat-node cluster
- Benefits:
 - Low OPEX:
 - No additional IT required for day-to-day operation
 - The need to manage only 4 'Fat-Nodes'
 - Internal storage is embedded in each 'Fat-Node'
 - Simplicity: InfiniBand performance without the complexity of managing such a solution

LARGE SCALE DEPLOYMENT WITHOUT THE COMPLEXITY





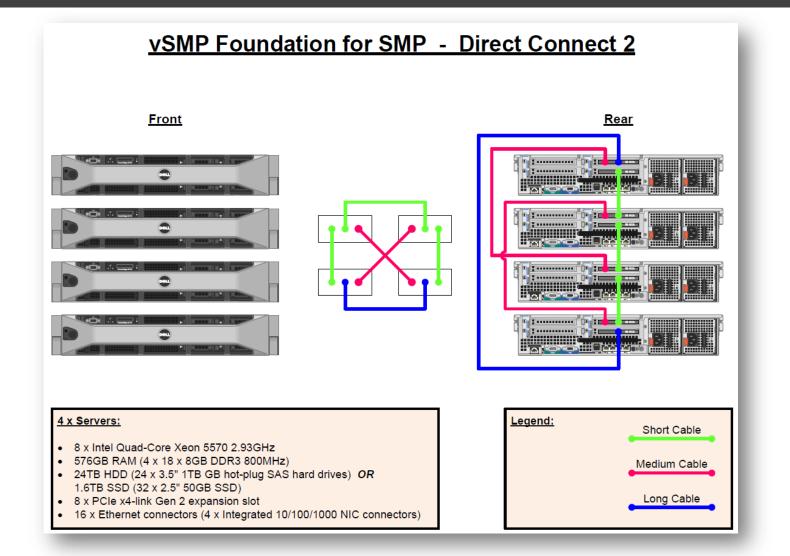
Solving Customer's Problems: Price & Performance

- Customer Pain:
 - Need faster results with less capital expenditure
 - Purchasing SMP is required but traditional SMP is too expensive
 - Cost of paying for future peak demand upfront
- vSMP Foundation Value:
 - Provide cost effective SMP using x86 commodity hardware
 - Providing more cores at speeds not limited by hardware attributes (virtual solution)
 - Pay as you grow for workloads requiring SMP
 - Supports distributed memory codes as efficiently as a cluster
- Result:
 - Faster run times and ability to run larger problems
 - Lower total cost of ownership than alternatives



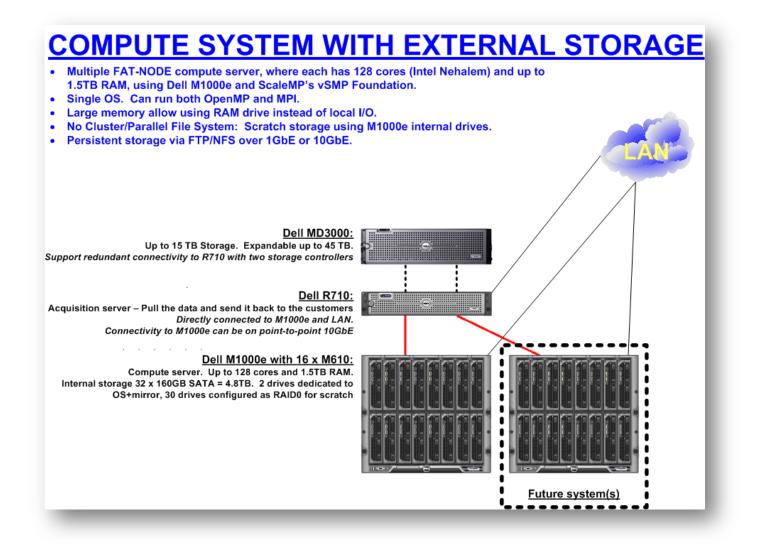


SMP 1: Direct Connect 2





SMP 2





Customer Use Cases

WEATHER FORECASTING SERVICE PROVIDER

- Customer: Weather forecasting service provider
- Current platform: SGI Altix with 32 cores
- Problems:
 - Need to shorten forecast compute times, without limited investment
 - Need to run MPI as well as OpenMP codes
 - System needs to be deployed remotely, and hence needs to be simple to manage
 - Data processing flow is complex and requires transferring large amounts of data between steps
- Applications: MM5, WRF, MAWSIP, Home-grown code for data transformation
- Solution:
 - 4 Intel Nehalem dual socket blades, total of 8 sockets (32 cores) and 192GB RAM
 - Using high-speed processors and internal storage for best performance
 - Extended to 8 blades, total of 16 sockets (64 cores) and 384GB RAM
- Benefits:
 - Performance: 2.5 X better performance on same # of cores (32)
 - **Cost:** Faster solution at the cost of annual maintenance of existing platform
 - Simplicity: Simple to manage by domain experts (weather forecast scientists)
 - Dataflow remains within the system, leveraging internal storage







Aggregate. Scale. Simplify. Save.

3/19/2010 24 Confidential and Proprietary

Customer Use Cases

MEDICAL RESEARCH INSTITUTE

- Customer: Medical Research Institute
- Current platform: HP Superdome System
- Problems:
 - Scanned data for a single run is currently over 200GB. Memory requirements
 are expected to grow significantly with the introduction of full body scan with more sensors
 - Execute high performance image processing on very large MRI scans
 - Would like the ability to use OpenMP and commercial tools for faster development
 - Would like to standardize on x86 architecture due to lower costs and open standards
- Applications: Siemens CT processing, MATLAB, BLAS, Home-grown code, ...
- Solution:
 - 16 Intel dual-processor Xeon systems to provide 1TB RAM, 32 sockets (128 cores) single virtual system running Linux with vSMP Foundation
- Benefits:
 - Performance: Solution evaluated and found to be faster than alternative systems
 - Cost: Significant savings compared to alternative system (order of magnitude)
 - Versatility: Also being used for MPI jobs as part of large cluster

LARGE MEMORY FOR MULTI-THREADED PROGRAMMING





Solving Customer's Problems: Inflexibility

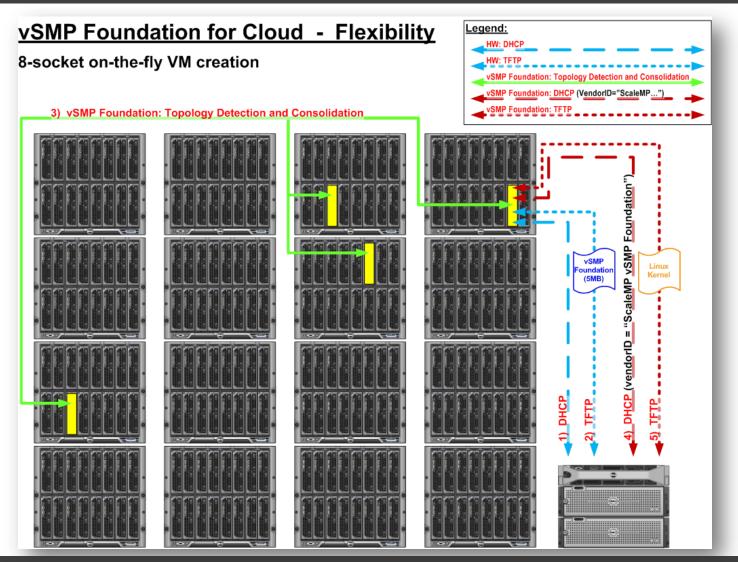
- Customer Pain:
 - Mix of distributed and SMP workloads requires dedicated infrastructure per workload
 - Overall system utilization
- vSMP Foundation Value:
 - Homogeneous commodity infrastructure for both distributed and SMP workloads
 - Ability to provision SMP nodes on-demand
 - Reduced OPEX using uniformed hardware infrastructure
- Result:
 - Lower TCO (CAPEX and OPEX)
 - Higher utilization rates optimized for customer workloads

Customer Exa	mples	<u>:</u>
	P	
SDSC SAN DIEGO SUPERCOMPUTER CENTER		



vSMP Foundation for Cloud

SINGLE INFRASTRUCTURE – FLEXIBLE RESOURCE PROVISIONING





Customer Use Cases

HOSTED HPC RESOURCE PROVIDER

- Customer: Hosted HPC resource provider
- Current platform: Clusters and large-memory machines
- Problems:
 - Need to run MPI as well as OpenMP (shared memory) codes
 - Large shared memory jobs require dedicated proprietary hardware requiring longer ROI period
 - Low utilization on dedicated shared memory systems
- Applications: A variety of commercial codes
- Solution:
 - Original: 4 systems, total of 8 sockets (32 cores) and 128GB RAM
 - Solution was extended to 16 nodes vSMP Foundation for Cloud
- Benefits:
 - Utilization: Rely on same standard commodity hardware for MPI, large memory, and OpenMP applications
 - Flexibility: Being able to provision multiple SMP systems when required, resulting in high utilization and higher income level

COST EFFECTIVE FLEXIBLE SOLUTION WITH HIGH UTILIZATION



Aggregate. Scale. Simplify. Save.

Customer Use Cases

SUPER COMPUTER CENTER

- Customer: San Diego Supercomputer Center (SDSC)
- Current platform: AMD 8 Socket Systems
- Problems:
 - Require an infrastructure for data intensive computing
 - Need large memory system (TBs in size), depending on job need
 - Require the ability to access quickly large amounts of storage
- Applications: A variety of data intensive codes (Astronomy, Genomics, Data Mining, etc..)
- Solution:
 - Initial Deployment: 4 X 'Super Nodes', each with 768GB RAM, 128 Cores, 10TB Internal Storage
 - Complete Deployment (2011): 1,024 servers with vSMP Foundation for Cloud. Could be aggregated up to 32 'Super Nodes' each nodes is 32 servers, resulting in 2TB RAM and 8TB of SSDs
 - On demand allocation using web-request and fast (<10 minutes) provisioning.
- Benefits:
 - Flexibility: Being able to provision multiple 'Super Nodes' on various sizes according to need
 - Performance: Extremely fast hierarchical memory solution: RAM -> Aggregated RAM -> Aggregated SSDs

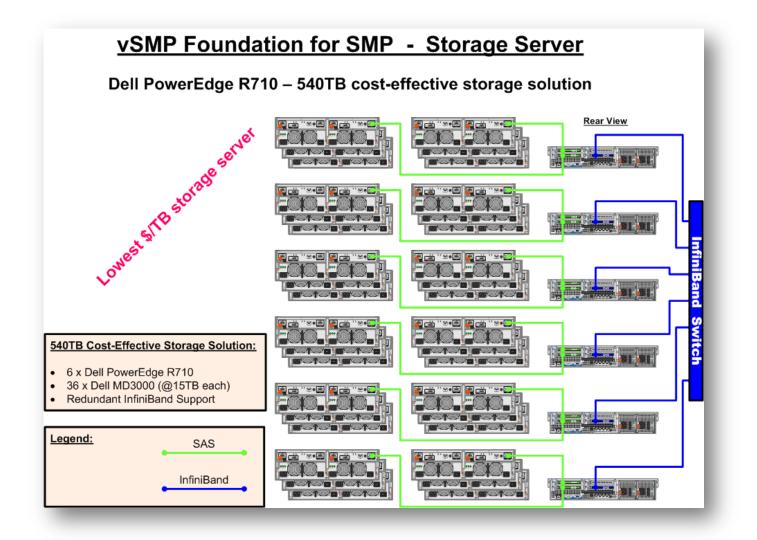
ELASTIC VM SOLUTION AIMED FOR DATA INTENTIVE COMPUTING





Storage: vSMP Foundation for SMP

NOT JUST COMPUTE - STORAGE SERVER SOLUTION







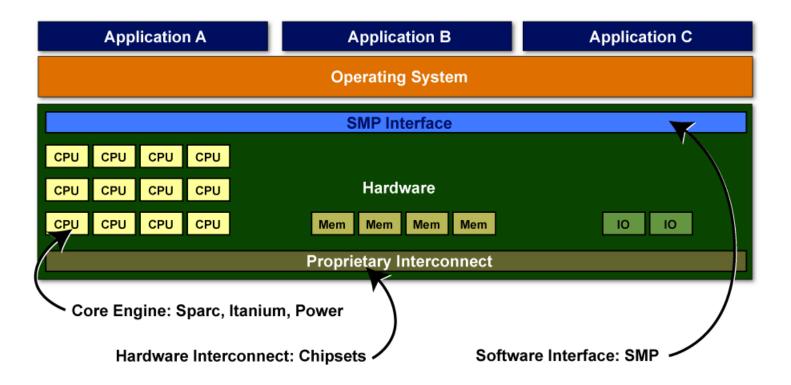
HOW DOES IT WORK



Aggregate. Scale. Simplify. Save.

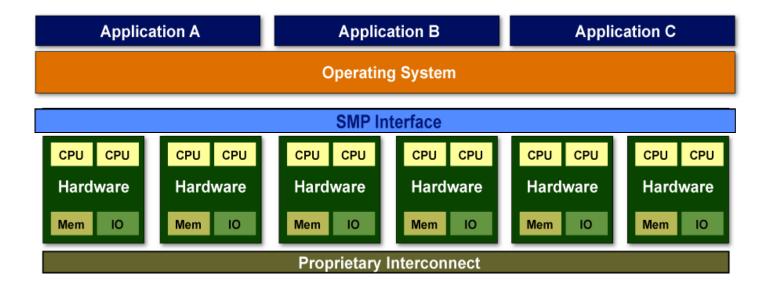
3/19/2010 31 Confidential and Proprietary

Traditional SMP



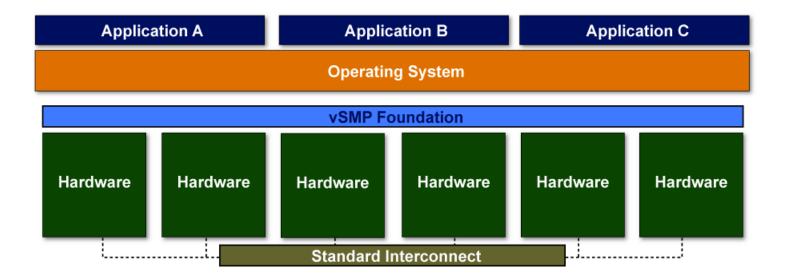


Evolving Traditional SMP





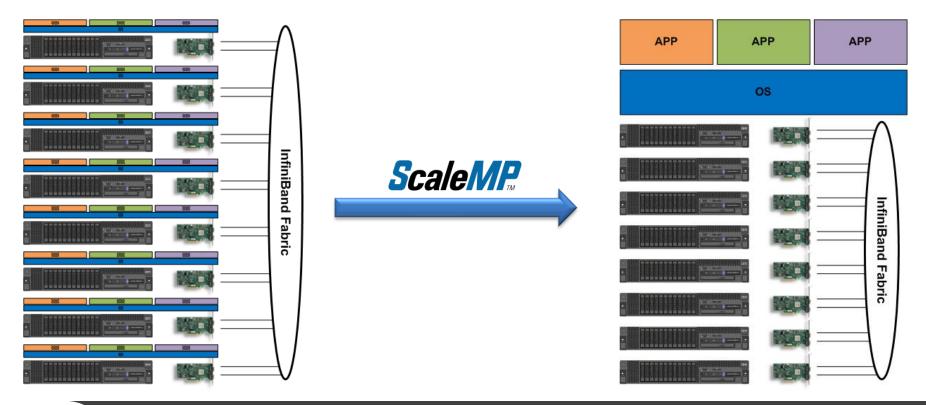
Evolving Traditional SMP





How Does it Work ?

Multiple Computers with Multiple Operating Systems Multiple Computers with a Single Operating System





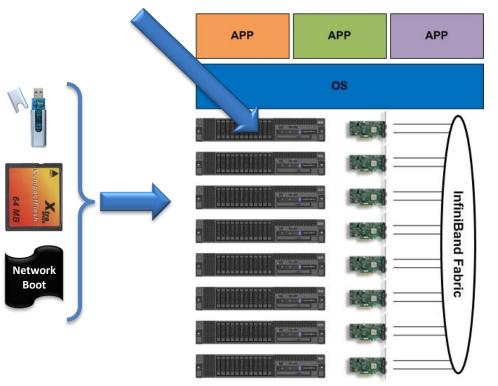
How Does it Work ?

Bare Metal, Distributed Virtual Machine Monitor

- Loaded at boot time
 - Supported boot devices: USB, IDE, CompactFlash or Network Image (PXE)
- Fabric probing and VM setup
- Loading the OS and maintaining I/O and memory coherency

Multiple Computers with a Single Operating System

Up to 16 servers (today), 128 servers (3Q2010)





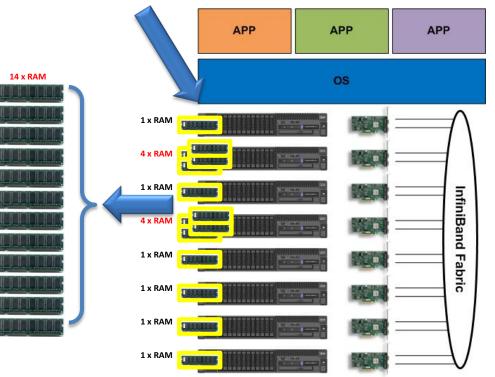
How Does it Work ?

Aggregated System

- Systems configuration can be different
 - Aggregating systems with different boards, I/O configurations, processors speed and memory configuration
 - Only one type of CPU will be presented to the OS
- >10 different coherency mechanisms
- Aggregated hardware I/O compatibility list include devices from Intel, Broadcom, LSI, ATI, Emulex, Adaptec and others

Multiple Computers with a Single Operating System

Up to 4TB aggregated (today), 64TB aggregated (3Q2010)





Behind The Scenes

One System

- Software interception engine creates a uniform execution environment
- vSMP Foundation creates the relevant BIOS environment to present the OS (and the SW stack above it) as single coherent system

Coherent Memory

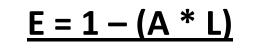
- vSMP Foundation maintains cache coherency between boards
- Multiple concurrent memory coherency mechanisms, on a per-block basis, based on real-time memory activity access pattern
- Leverage board local-memory for caching

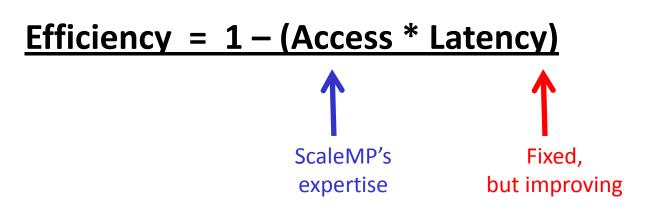
Shared I/O

- vSMP exposes all available I/O resources to the OS in a unified PCI hierarchy
- No need for cluster file systems



Coherent Memory: Efficiency Formula







Coherent Memory: Basics

Trade backplane-latency with redundant RAM

- Hiding backplane latency using software-driven sophisticated and adaptive caching techniques
- Better system economics leveraging PC economies of scale: memory cost vs. propriety backplane/chipset





PERFORMANCE EXAMPLES



Aggregate. Scale. Simplify. Save.

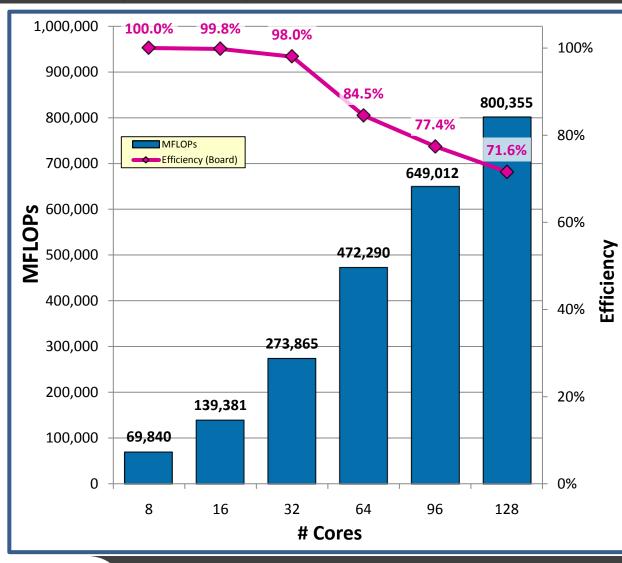
3/19/2010 41 Confidential and Proprietary

OPENMP PARALLELIZATION (1)

Last Update: 9/1/2009

DGEMM (INTEL MKL)

ScaleMP



Aggregate. Scale. Simplify. Save.

- MKL is Intel's Math Kernel Library, which is using threads for parallelization and is the corner stone for many applications.
- DGEMM is the Matrix Multiply function which is the base for many numerical algorithms. Matrix size used 17,000 X 17,000.
- vSMP Foundation demonstrates over 70% efficiency scaling across 16 boards (128 cores).
- System configuration:
 - /vSMP Foundation (16 nodes) Data intensive supercomputer system - 128 cores (32 sockets), 768 GB RAM
 - 16 X Dual-socket servers (Intel Xeon E5530 2.40 GHz, 48 GB RAM)

Threaded

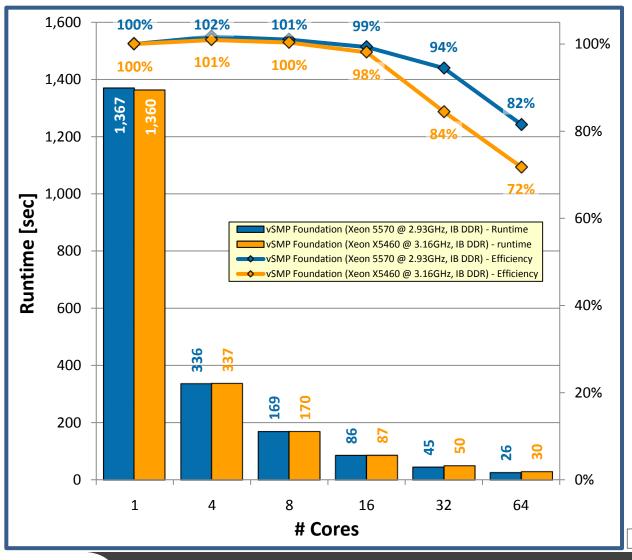
3/19/2010 42 Confidential and Proprietary

OPENMP PARALLELIZATION (2)

Last Update: 9/1/2009

LANCZOS (SMALL)

ScaleMP.



Aggregate. Scale. Simplify. Save.

- Customer custom code for calculating eigenvalues leveraging OpenMP for parallelization
- vSMP Foundation demonstrates close to linear scalability using OpenMP:
 - 82% Efficiency with 64 CPUs (Intel Xeon X5570)
 - 72% Efficiency with 64 CPUs (Intel Xeon X5460)

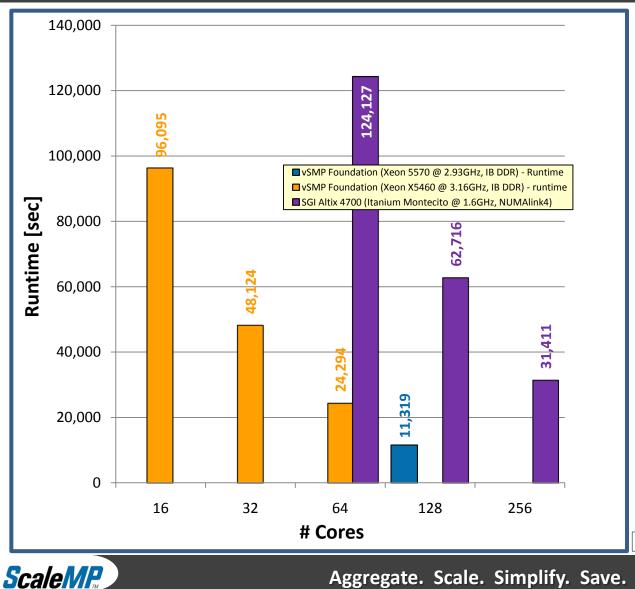
Threaded

3/19/2010 43 Confidential and Proprietary

OPENMP PARALLELIZATION (3)

Last Update: 9/1/2009

LANCZOS (LARGE)



Aggregate. Scale. Simplify. Save.

- Customer custom code for calculating eigenvalues leveraging OpenMP for parallelization
- vSMP Foundation demonstrates close • to linear scalability using OpenMP
- vSMP Foundation is faster than SGI • Altix
 - 11x faster on 64 cores
 - 3x faster with 128 cores compared to Altix with 256 cores

Threaded

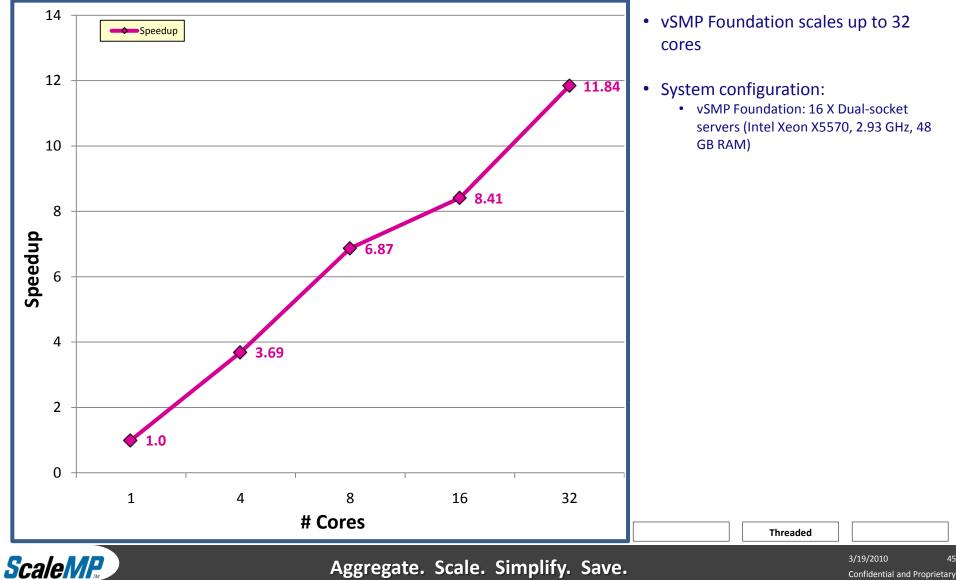
3/19/2010 Confidential and Proprietary

Δ1

GAUSSIAN

Last Update: 9/1/2009

397 BENCHMARK

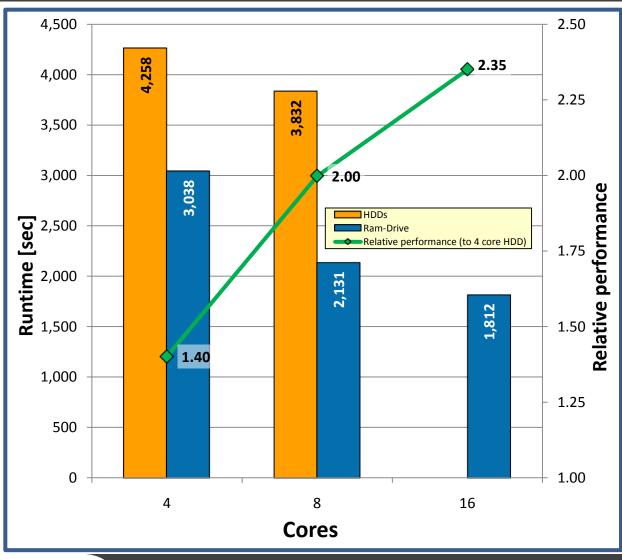


Confidential and Proprietary

GAUSSIAN: LARGE MEMORY VS. I/O

Last Update: 9/1/2009

CUSTOMER MOLECULE



- Comparison of Gaussian workload with limited scalability due to extensive I/O
 - 1 Board system using HDDs
 - Aggregated system using with vSMP Foundation, enabling RAM-drive for I/O
- vSMP Foundation provides improved performance:
 - Scales with aggregated memory for I/O (using RAM-drive)
 - 2.0 X faster compared to HDD performance (8 cores)
 - 2.35 X faster with higher core count (16 cores)
- System configuration:
 - vSMP Foundation: 16 X Dual-socket servers (Intel Xeon X5570, 2.93 GHz, 48 GB RAM)
 - Comparable system: Dual Socket (Intel X5570, 2.93 GHz, 48 GB RAM)

Threaded

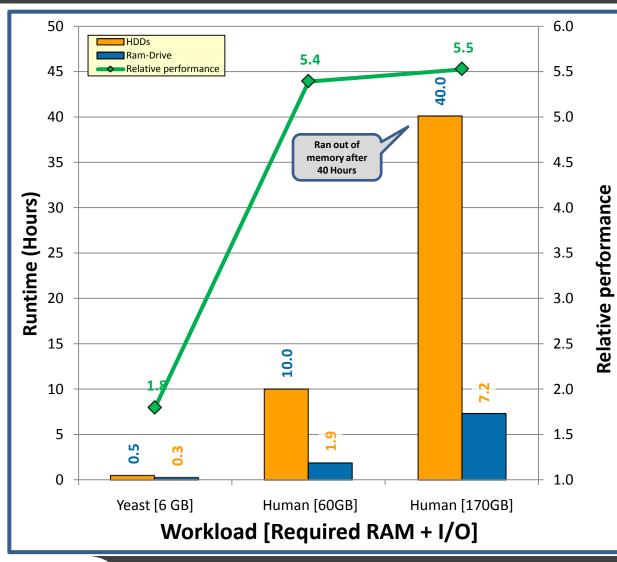
Large Memory



454 NEWBLER

Last Update: 9/1/2009

DNA SEQUENCING



- Performance comparison of DNA Sequencing workload
- Large amounts of memory required, performs extensive I/O and is not using more than 4-8 cores during most of the run
- vSMP Foundation provides improved performance:
 - Running large sequencing inputs which are not feasible otherwise
 - Taking advantage of aggregated RAM for I/O
 - vSMP Foundation achieves 2 to 5 X better performance
- System configuration:
 - Ram-drive: vSMP Foundation: 16 X Dualsocket servers (Intel Xeon X5570, 2.93 GHz, 48 GB RAM)
 - HDD: Dual Socket (X5570 2.93 GHz, 24GB RAM)

Large Memory

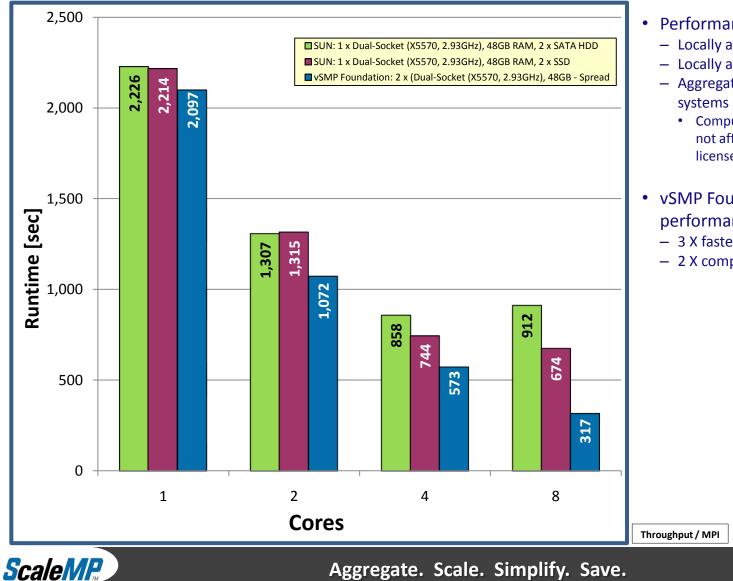


Aggregate. Scale. Simplify. Save.

3/19/2010 47 Confidential and Proprietary

MSC NASTRAN (2)

LARGE MEMORY VS. I/O: XL0TDF1



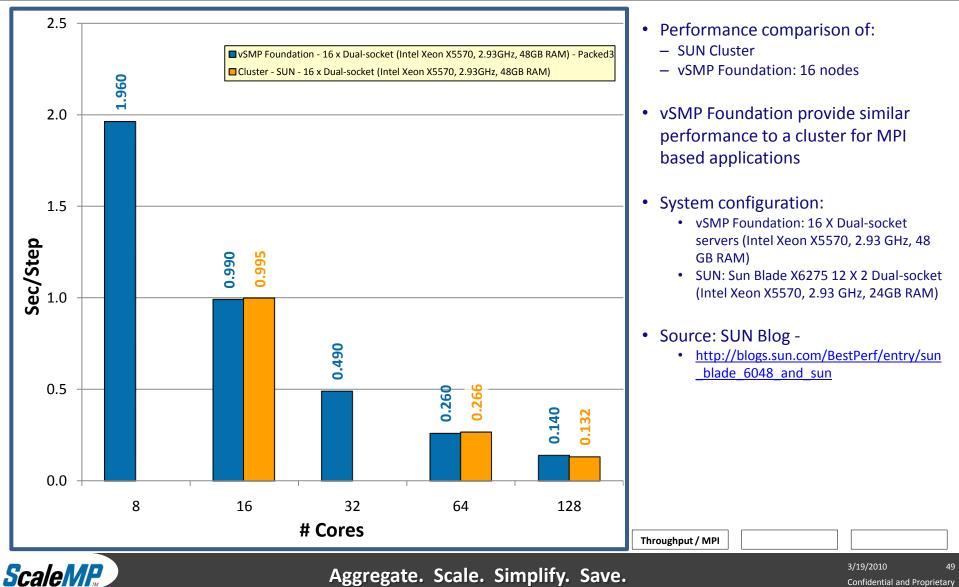
Aggregate. Scale. Simplify. Save.

- Performance comparison of:
 - Locally attached HDDs
 - Locally attached SSDs
 - Aggregated memory and CPUs of 2 systems with vSMP Foundation
 - Compute utilized 4 cores on each system, not affecting the application (NASTRAN) license cost
- vSMP Foundation provide significant performance gains:
 - 3 X faster compared to HDDs
 - 2 X compared to SSDs

Large Memory

NAMD

STMV MOLECULE



Confidential and Proprietary



MORE INFORMATION



Aggregate. Scale. Simplify. Save.

3/19/2010 50 Confidential and Proprietary

More Information

http://www.scalemp.com

http://www.scalemp.com/performance

info@scalemp.com





THE HIGH-END VIRTUALIZATION COMPANY SERVER AGGREGATION – CREATING THE POWER OF ONE

Nir Paikowsky Director of Application Engineering

nir@ScaleMP.com, +1 (650) 283 2110